

QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>101642,630</u>	Prepared by <u>NH</u>	Tracking Number <u>05919715</u>	
Examiner-GAU <u>Kim - 2871</u>	Date <u>4-20-4</u>	Week Date <u>03/15/04</u>	
	No. of queries <u>1</u>	<u>IFW</u>	

JACKET			
a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION	
a. Page Missing	MESSAGE PTD-1449: Please either initial or line through citations. Copy provided for reference.
b. Text Continuity	
c. Holes through Data	
d. Other Missing Text	
e. Illegible Text	
f. Duplicate Text	
g. Brief Description	
h. Sequence Listing	
i. Appendix	
j. Amendments	
k. Other	
CLAIMS	
a. Claim(s) Missing	Thank you
b. Improper Dependency	
c. Duplicate Numbers	
d. Incorrect Numbering	
e. Index Disagrees	initials NH
RESPONSE	
f. Punctuation	
g. Amendments	
h. Bracketing	
i. Missing Text	
j. Duplicate Text	
k. Other	
	initials

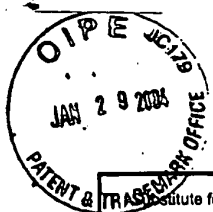


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 241287US8CONT		SERIAL NO. 10/642,630	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Kazunori MUKASA, et al.			
				FILING DATE August 19, 2003		GROUP 2874	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO	0 938 001	08/25/1999	EUROPE			
	AP	0 959 374	11/24/1999	EUROPE			
	AQ	11-84159	03/26/1999	JAPAN (with English Abstract)			X
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	L. G. COHEN, et al., Electronics Letters, vol. 18, no. 24, XP-000543480, pages 123-1024, "LOW-LOSS QUADRUPLE-CLAD SINGLE-MODE LIGHTGUIDES WITH DISPERSION BELOW 2 ps/km nm OVER THE 1.28 μ m - 1.65 μ m WAVELENGTH RANGE", November 25, 1982					
	AX	H. HATAYAMA, et al., General National Meeting of the Institute of Electronics, Information and Communication Engineers, XP-002946628, page 225, "DISPERSION FLATTENED FIBER WITH LARGE EFFECTIVE AREA", March 6, 1998					
	AY	D. W. PECKHAM, et al., Proceedings of the European Conference on Optical Communication, vol. 1, no. 1, XP-002115607, "REDUCED DISPERSION SLOPE, NON-ZERO DISPERSION FIBER", September 20, 1998					
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>091832,685</u>	Prepared by <u>NH</u>	Tracking Number <u>05926800</u>	
Examiner-GAU <u>Chang-2819</u>	Date <u>4-19-4</u>	Week Date <u>03/29/04</u>	
	No. of queries <u>1</u>	IFW	

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f. Punctuation	RESPONSE
g. Amendments	
h. Bracketing	
i. Missing Text	
j. Duplicate Text	
k. Other	
	initials



INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete If Known			
		Application Number	09/832,685		
		Filing Date	April 10, 2001		
		First Named Inventor	Reddy, Srinivas		
		Art Unit	2819		
		Examiner Name	Daniel D. Chang		
Sheet	3	of		Attorney Docket Number	015114-045720US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	0033	Scarpero, Jr., Field-Effect Transistor Bidirectional Driver Control Circuit, IBM TDB, vol. 16, No. 8, pp. 2442-2443, Jan. 1974.	
	0034	Rhyne, Fundamentals of Digital Systems Design, N. J., pp 70-71, 1973.	
	0035	Anonymous, Tri-State Double inverter Buffer, Research Disclosure, p. 30511, Sep. 1989.	
	0036	Xilinx Corp., The Programmable Logic Data Book, "XC4000, XC4000A, XC4000H, Logic Cell Array Families," pp. 2-7 to 2-46 (1994).	
	0037	Xilinx Corp., The Programmable Logic Data Book, "XC3000, XC3000A, XC3000L, XC3100, XC3100A, Logic Cell Array Families," pp. 2-105 to 2-124 (1994).	
	0038	Xilinx Corp., The Programmable Logic Data Book, "XC3000, XC3000A, XC3000L, XC3100, XC3100A, Logic Cell Array Families," pp. 2-105 to 2-152 (1994).	
	0039	Altera Data Book, Altera Classic, Programmable Logic Device Family, pp. 235-244, 08/93.	
	0040	Altera Data Book, Max 5000 Programmable Logic Device Family, pp. 149-160, 08/93.	
	0041	Altera Data Book, Max 7000 Programmable Logic Cell Device Family, pp. 69-81, 08/93.	
	0042	Altera Data Book, Flex 8000 Programmable Logic Device Family, pp. 29-47, 08/93.	
	0043	Altera Data Book, Introduction, pp. 3-16, 08/93.	

Examiner Signature		Date Considered	
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.